


[Web](#) [Images](#) [Groups](#) [News](#) [Froogle](#) [more »](#)


[Advanced Search](#)  
[Preferences](#)

## Web

 Results 1 - 10 of about 36,200 for **PLD configuration**. (0.49 seconds)

### ARM Technical Support - Downloads - Versatile - Versatile FPGA and ...

... Home > Technical Support > Downloads > Versatile > Versatile FPGA and **PLD configuration** images. ... Versatile FPGA and **PLD configuration** images, ask ARM, ...  
[www.arm.com/support/downloads/info/4804.html](http://www.arm.com/support/downloads/info/4804.html) - 24k - [Cached](#) - [Similar pages](#)

### ARM Technical Support - Downloads - Integrator

... Schematics, Netlist and Bill of Materials for all ARM10 core modules Read Summary, Download ( 1114 Kb Zip), Integrator FPGA and **PLD configuration** images, ...  
[www.arm.com/support/downloads/integrator.html](http://www.arm.com/support/downloads/integrator.html) - 41k - [Cached](#) - [Similar pages](#)  
 [ [More results from www.arm.com](#) ]

### Xilinx: Programmable Logic Devices, FPGA & CPLD - News Release

... "The XC1800 PROM series demonstrates the Xilinx commitment to industry leadership in **PLD configuration** memories. No other devices ...  
[investor.xilinx.com/phoenix.zhtml?c=75919&p=irol-newsArticle&t=Regular&id=52147&](http://investor.xilinx.com/phoenix.zhtml?c=75919&p=irol-newsArticle&t=Regular&id=52147&) - 32k - [Cached](#) - [Similar pages](#)

### [PDF] Implementing the MicroBlaster Configuration on the ColdFire ...

File Format: PDF/Adobe Acrobat  
 ... RBF download To download the RBF bit stream into the ADRAM for **PLD configuration**. ...  
**Configuration** To configure the APEXE 20K200E device on the **PLD** test board. ...  
[www.altera.com/literature/wp/wp\\_mcrblstr.pdf](http://www.altera.com/literature/wp/wp_mcrblstr.pdf) - [Similar pages](#)

### [PDF] EPC16 Device Pin-Outs Pin Name Pin Type Description 88- Pin Ultra ...

File Format: PDF/Adobe Acrobat  
 ... Input External clock source H3 DCLK Output **Configuration** device drive this signal to **PLD** as **configuration** clock A10 DATA0 Output **PLD configuration** output data ...  
[www.altera.com/literature/dp/config/epc16\\_88.pdf](http://www.altera.com/literature/dp/config/epc16_88.pdf) - [Similar pages](#)  
 [ [More results from www.altera.com](#) ]

### [PDF] Collider-Accelerator Department Brookhaven National Laboratory ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)  
 ... name, version) 39,3d / D08(OE) 16 byte (depend on IO **PLD** version) 20000 - 3fff  
 Flash Memory 39,3d / D08(OE) 128 Kbytes (for IO **PLD configuration** file .rbf ...  
[www.agsrhichome.bnl.gov/Hardware/tian/V128/v128\\_spec.PDF](http://www.agsrhichome.bnl.gov/Hardware/tian/V128/v128_spec.PDF) - [Similar pages](#)

### [PDF] A NOVEL PLD CONFIGURATION FOR DEPOSITION OF FILMS OF IMPROVED ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)  
 A NOVEL **PLD CONFIGURATION** FOR DEPOSITION OF FILMS OF IMPROVED QUALITY: A CASE STUDY ON CARBON NITRIDE T. Szörényi and B. Hopp, Research Group on Laser Physics ...  
[www.iesl.forth.gr/cola03/presentations/pdf/C005.pdf](http://www.iesl.forth.gr/cola03/presentations/pdf/C005.pdf) - [Similar pages](#)

### PLD Components

... Typical **PLD Configuration** and Variations. ... Example 3. Simple **PLD Configuration** This design offers basic substrate and target devices with no XY or Z adjustment. ...  
[www.thermionics.com/pld.htm](http://www.thermionics.com/pld.htm) - 8k - [Cached](#) - [Similar pages](#)

### EP1215586

... The options register contains a predetermined number of bits, which indicate, for

example, whether **PLD configuration** data follows the current transfer, how the ...  
swpat.ffii.org/pikta/txt/ep/1215/586/ - 20k - [Cached](#) - [Similar pages](#)

[PDF] [Suggestions for a \*\*Configuration\*\* Standard for the Programmable ...](#)  
File Format: PDF/Adobe Acrobat - [View as HTML](#)  
... Passive Serial (Altera) As in the case of the Slave Serial **configuration** mode, the  
Altera **PLD** receives bit-wide **configuration** data and the corresponding clock ...  
pcweb.physik.uni-giessen.de/hades/docs/tech/Conf\_Std\_160998.pdf - [Similar pages](#)

Google

Result Page:    [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#)    [Next](#)

Free! Get the Google Toolbar. [Download Now](#) - [About Toolbar](#)



[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2004 Google